

In re Patent Application of:
PASOTTI ET AL.
Serial No. Not Yet Assigned
Filed: Herewith

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In the Specification:

Please replace paragraph 0009 with the following,
re-written paragraph:

--In accordance with the invention, an architecture for distributing supply voltages to a plurality of memory modules supplied through a plurality of charge pump circuits may include an arbitrator block bi-directionally connected to the plurality of memory modules, from which it may receive a plurality of power requests. The arbitrator block may sort the power requests based upon a priority scale to drive the plurality of charge pump circuits, and distribute supply voltages to the plurality of memory modules. The architecture may advantageously be software-configurable.--